

## sdmay19-41: Wideband Full Stokes parameterised spectrometer for Xilinx RFSoc Platform

### Week 1 Report

August 20 - September 7

### Team Members

Brian Bradford — *DSP Engineer and Meeting Facilitator*  
 Louis Hamilton — *FPGA Engineer and Meeting Scribe*  
 Jared Danner — *FPGA Engineer and Report Manager*  
 Nick Knuth — *FPGA Engineer*  
 Vishal Joel — *GUI Developer and FPGA Engineer*

### Summary of Progress this Report

Researched documentation on project topic to learn more background information. We also met to talk over the project and roles.

### Pending Issues

Uncertainty about amount of work needed for Board Support Package

### Plans for Upcoming Reporting Period

More research. Look into RFSoc eval board documentation to see what all needs to be implemented, migen, and existing Toolflow.

### Individual Contributions

Team Member	Contribution	Weekly Hours	Total Hours
Brian Bradford	Researched documentation on project topic to learn more background information.	8	8
Louis Hamilton	Researched documentation on project topic to learn more background information.	8	8
Jared Danner	Researched documentation on project topic to learn more background information.	8	8
Nick Knuth	Researched documentation on project topic to learn more background information.	8	8
Vishal Joel	Researched documentation on project topic to learn more background information.	8	8

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**Gitlab Activity Summary**

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Action: joined, Tue Aug 28 2018  
Author: louis

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Action: joined, Tue Aug 28 2018  
Author: bjbford

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Action: joined, Tue Aug 28 2018  
Author: jdanner

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Action: joined, Tue Aug 28 2018  
Author: joel16

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Action: created, Tue Aug 28 2018  
Author: sd  
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